

What Is Claimed Is:

1. A method for serially transmitting data between a first and a second station, the first station unidirectionally transmitting at least two signals to the second station on two signal paths, wherein a shift register is provided in each station, the two signal paths of the first station being parallelly routed into a shift register, and the data transmission to the second station being carried out by automatically clocking the shift register from a time base.
2. The method as recited in Claim 1, wherein the automatic timing of the shift register from a time base of the first station takes place in such a manner, that the shift register receives a clock signal of the time base, and the shift register automatically transmits the data with the aid of this clock signal.
3. The method as recited in Claim 1, wherein the automatic timing is carried out at a clock-pulse rate, which is at least twice as high as a signal rate that results from the resolution of the signal of the at least two signals, which has the higher resolution.
4. The method as recited in Claim 1, wherein the signals are in the form of pulse-width modulated signals.
5. The method as recited in Claim 1, wherein the at least two signals each have a high signal level and a low signal level, the high signal level and the low signal level of the at least two signals being, in each instance, equal within specifiable tolerances.
6. The method as recited in Claim 1, wherein the automatic timing takes place at a clock-pulse

rate, and the cycle time resulting from the clock-pulse rate is less than or equal to a slope time of the signals to be transmitted.

7. A device for serially transmitting data between a first and a second station, the first station unidirectionally transmitting at least two signals to the second station on two signal paths,

wherein a shift register is provided in each station, the two signal paths of the first station being parallelly routed into a shift register, and a time base being included, with the aid of which the data transmission to the second shift register of the second station is implemented by automatically clocking at least the first shift register.

8. The device as recited in Claim 7,  
wherein a timer module, which automatically generates a time signal at fixed intervals, is provided as a time base.